



# Dual Priority Scheduling algorithm used in the nMPRA Microcontrollers

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prof.dr.eng. **Vasile Gheorghită GĂITAN**

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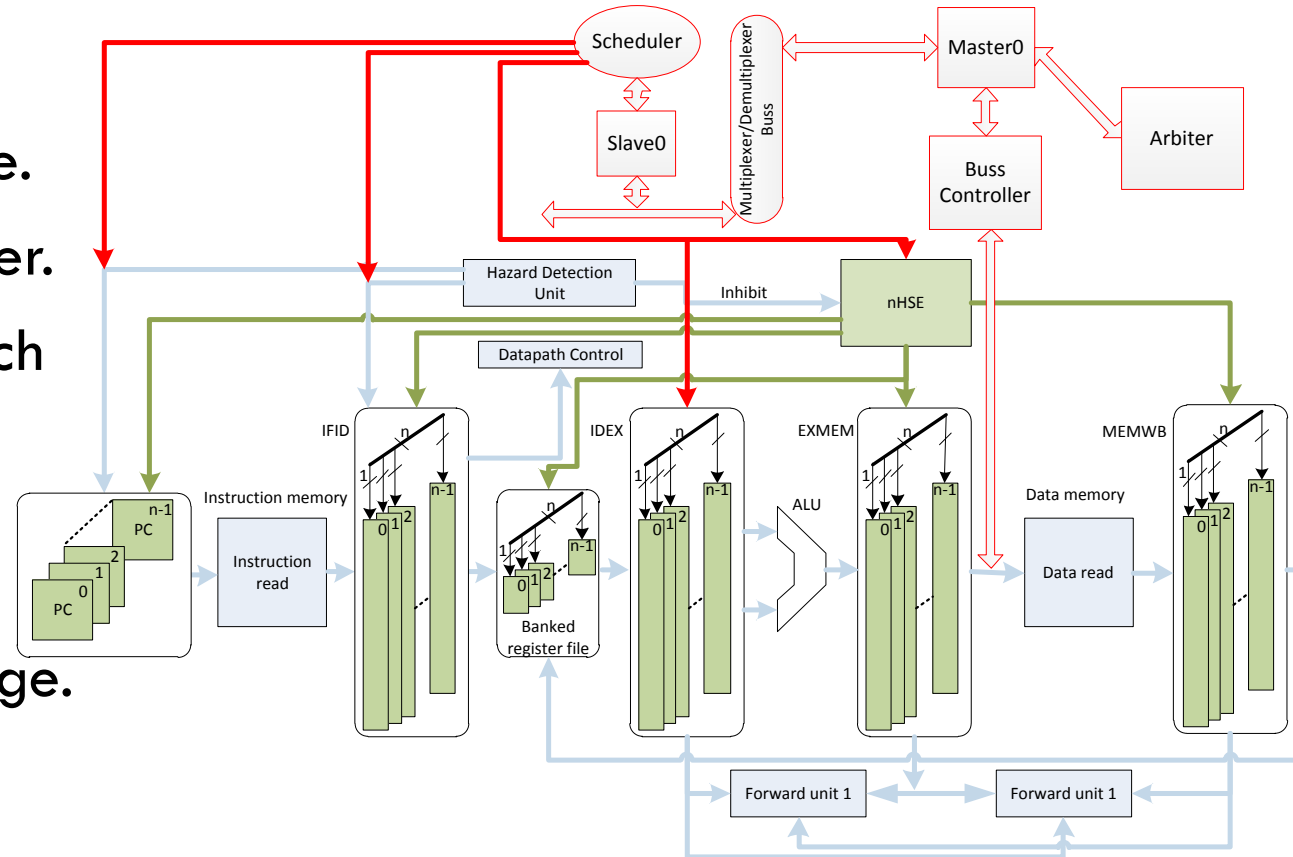
# 1. Why?

- The use of a single scheduling algorithm can lead to the following problems:
  - most of the real time operating systems are certified for tasks that are much pessimistic that the designer will thought.
  - In the automotive field, the non-preemptive schedulers are used where the system is safety relevant. The adoption of this strategy is used because offers a high predictability to the system, but can lead the system into failure.

# 2. Overview(1)

The nMPRA architecture.

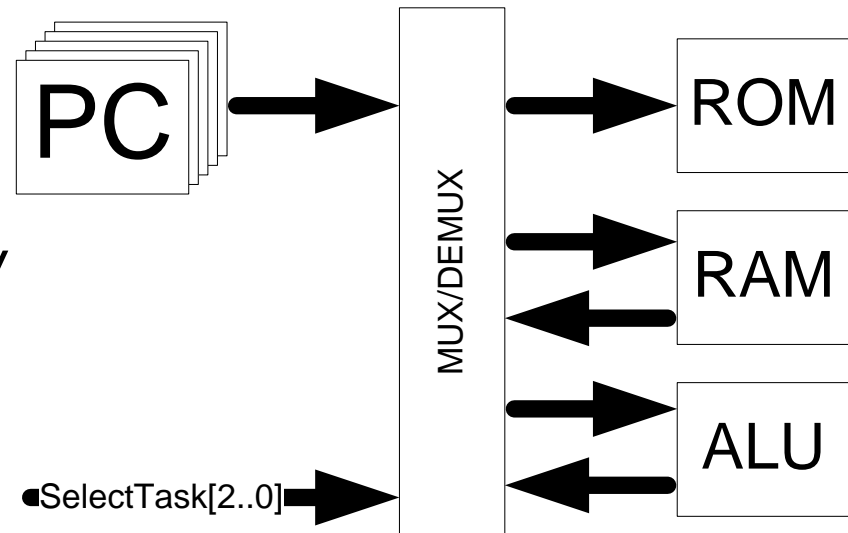
- PC – program counter.
- IFID – Instruction Fetch Instruction Decode stage.
- ID/EX – Instruction decode–execute stage.
- EX/MEM – execute–memory stage.
- MEM/WB – memory–write back stage.



# 2. Overview(2)

The simplified nMPRA architecture.

- The RAM, ALU and ROM are shared between multiple resources using only one big multiplexer/demultiplexer.
- Each task will share only RAM, ALU, ROM. The register file and the program counter will be multiplied. This approach will ensure that the data will remain valid after every task switch.



# 3. Dual priority algorithm (1)

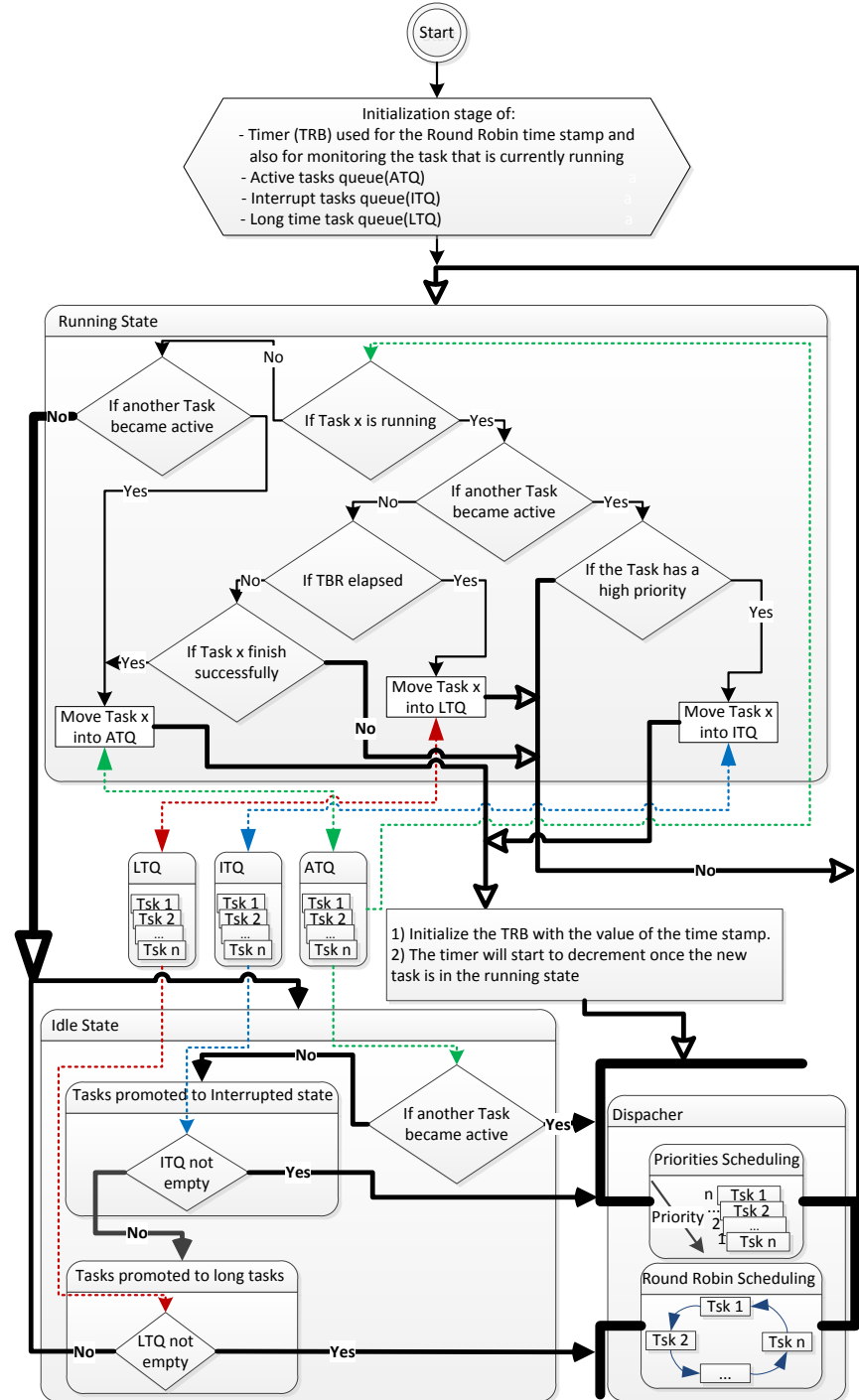
Classes that each task can belong to the dynamic dual priority algorithm :

- The class of active tasks, which has the higher priority (the tasks will be inserted only in the active task queue (ATQ)) will schedule the tasks, based on priorities, only in the Running State (RS) of the Scheduler.
- The class of interrupted tasks, which has the second priority (the tasks will be inserted only in the interrupted task queue (ITQ)) will schedule the tasks, based on priorities, only in the Idle State (IS) of the Scheduler.

# 3. Dual priority algorithm (2)

- The long execution tasks class (significantly exceed the base period  $T$  corresponding to the priority task), which has the least priorities (LTQ): will schedule the tasks, based on ROUND ROBIN (RR) algorithm, only in the Idle State (IS) of the Scheduler.

- ATQ-active task queue.
- ITQ-interrupted task queue.
- LTQ-long task queue.
- TRB-timer Round Robin used to supervise the active time of the current task and also used as a task occurrence for the long tasks.
- Running State-processor execute code.
- Idle State-the processor finishes the execution of code.





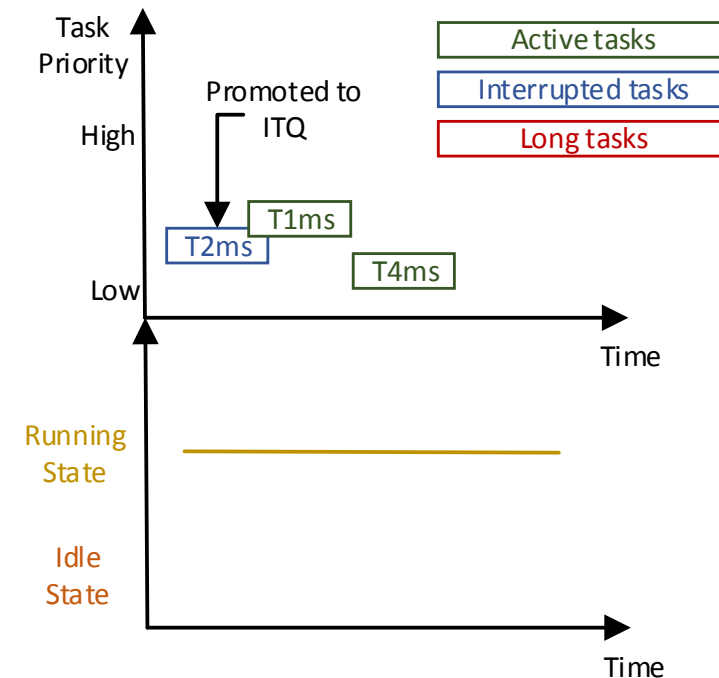
# 3. Dual priority algorithm (3)

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- If the TRB and the occurrence of the tasks are not chosen properly, the following situation can occur:
  - ▣ Priority inversion.
  - ▣ Task starvation.

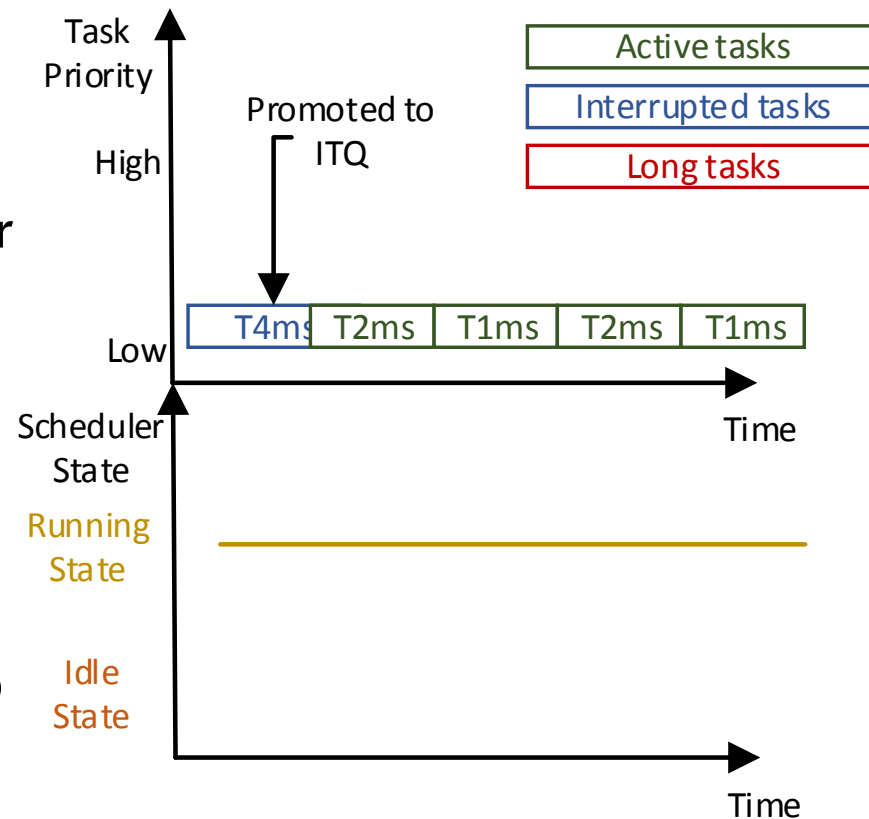
# 3. Dual priority algorithm (4)

- Priority inversion: There are 3 tasks with different priorities. The task, with intermediary priority, is running and is interrupted by the second task with higher priority. The task that was interrupted is now in the ITQ and the current task finishes its execution, but at the same time, the third task, with lower priority became active. In this particularly case the task from ITQ will not run, even though it has higher priority, because the new task is located into the ATQ.



# 3. Dual priority algorithm (5)

- Task starvation: Assume that we have 3 tasks with different recurrence and same priorities. If one task is interrupted by another task, with higher priority, this task will be introduced into the ITQ. From this point forward all of the tasks are going to succeed properly forcing the Scheduler to remain in the RS. Because the ITQ and LTQ are used only in the IS, this task will starve.



# 3. Dual priority algorithm (6)

- If these two requirements are not met, the scheduler will not work properly because one of the two unfortunate situations, explained above, will happen. This situation is not a consequence of the scheduling algorithm being implemented in hardware, the same situation could have happened to a scheduling algorithm implemented in software.
- If the system is overloaded with tasks the system can be configured to work only in a Round Robin manner, each task will have the same occurrence.

# 4. Conclusions(4)

- The algorithm presented will ensure the execution of each task, even for those states that are different from normal state, only if the occurrence of the tasks and the time for the TRB is chosen properly.
- The nMPRA microcontroller provides very good switching time, constant 5 machine cycles.
- Reduce the consumption of memory of the Operating System(ROM, RAM).

# 5. References

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October 17 - 19, 2014

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October 17-19, 2014, Sinaia, Romania

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Univ. of Craiova

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Cinvestav

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Univ. of Melbourne

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Gheorghe Asachi Tech. Univ. of Iasi

[Pastravanu, Octavian-Cezar](#)

Gheorghe Asachi Tech. Univ. of Iasi

[Voicu, Mihail](#)

Gheorghe Asachi Tech. Univ. of Iasi

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Gheorghe Asachi Tech. Univ. of Iasi

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Lucian Blaga Univ. of Sibiu

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Univ. of Craiova

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Co-Chair: [Muresan, Cristina Ioana](#) Tech. Univ. of Cluj Napoca  
Organizer: [Copot, Cosmin](#) Ghent Univ.  
Organizer: [Muresan, Cristina Ioana](#) Tech. Univ. of Cluj-Napoca

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[Ionescu, Clara](#) Ghent Univ.  
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[Melicio, Rui](#) Univ. De Evora  
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Pol. Univ. of Bucharest  
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[Serbencu, Adrian Emanoil](#) Dunarea De Jos Univ. of Galati  
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Co-Chair: [Costeniuc, George](#) Continental Automotive Romania SRL Iasi

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Pol. Univ. of Bucharest

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[Resceanu, Ionut Cristian](#)

Univ. of Craiova

[Resceanu, Cristina Floriana](#)

Univ. of Craiova

[Simionescu, Sabin Mihai](#)

SC White Pyramid SRL

**FrB1 Invited Session, Miorita**

**[New Trends in Robotic Systems](#)**

Chair: [Cervera, Enric](#)

Jaume-I Univ. of Castello de la Plana

Co-Chair: [Panescu, Doru-Adrian](#)

Gheorghe Asachi Tech. Univ. of Iasi

Organizer: [Cervera, Enric](#)

Jaume-I Univ. of Castello de la Plana

Organizer: [Borangiu, Theodor](#)

Pol. Univ. of Bucharest

Organizer: [Panescu, Doru-Adrian](#)

Gheorghe Asachi Tech. Univ. of Iasi

Organizer: [Burlacu, Adrian](#)

Gheorghe Asachi Tech. Univ. of Iasi

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Pol. Univ. of Bucharest

[Borangiu, Theodor](#)

Pol. Univ. of Bucharest

[Ivanescu, Nick Andrei](#)

Pol. Univ. of Bucharest

[Morariu, Octavian](#)

Pol. Univ. of Bucharest

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Gheorghe Asachi Tech. Univ. of Iasi

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Gheorghe Asachi Tech. Univ. of Iasi

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Gheorghe Asachi Tech. Univ. of Iasi

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Gheorghe Asachi Tech. Univ. of Iasi

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Gheorghe Asachi Tech. Univ. of Iasi

[Caruntu, Constantin - Florin](#)

Gheorghe Asachi Tech. Univ. of Iasi

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Pol. Univ. of Bucharest

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Pol. Univ. of Bucharest

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[Condurache, Daniel](#)

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**FrB2 Invited Session, Ara**

**[Real Time Systems Applications](#)**

Chair: [Popescu, Dumitru](#)

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Co-Chair: [Lupu, Ciprian](#)

Pol. Univ. of Bucharest

Organizer: [Popescu, Dumitru](#)

Pol. Univ. of Bucharest

Organizer: [Lupu, Ciprian](#)

Pol. Univ. of Bucharest

16:10-16:30, [Paper FrB2.1](#)

[\*Emergent Intelligence in Agents: A Scalable Architecture for Smart Cities \(I\)\*](#)

[Patrascu, Monica](#)

Pol. Univ. of Bucharest

[Dragoicea, Monica](#)

Pol. Univ. of Bucharest

[Ion, Andreea](#)

Pol. Univ. of Bucharest

16:30-16:50, [Paper FrB2.2](#)

[\*Real Time Agent Based Simulation for Smart City Emergency Protocols \(I\)\*](#)

[Dragoicea, Monica](#)

Pol. Univ. of Bucharest

[Patrascu, Monica](#)

Pol. Univ. of Bucharest

[Serea, George Alexandru](#)

Pol. Univ. of Bucharest

16:50-17:10, [Paper FrB2.3](#)

[\*An Approach for Load Balancing in Virtual Power Plant Structures \(I\)\*](#)

[Lupu, Ciprian](#)

Pol. Univ. of Bucharest

[Oancea, Dumitru](#)

Pol. Univ. of Bucharest



[Oara, Cristian](#) Pol. Univ. of Bucharest  
[Lupu, Mircea](#) Transilvania Univ. of Brasov  
[Apetrei, Dan](#) ELSACO Energy

17:10-17:30, [Paper FrB2.4](#)

*Real-Time Acquisition of Quality Verified Nonstandardized Color Images for Skin Lesions Risk Assessment – a Preliminary Study (I)*

[Udrea, Andreea](#) Pol. Univ. of Bucharest  
[Lupu, Ciprian](#) Pol. Univ. of Bucharest

17:30-17:50, [Paper FrB2.5](#)

*Designing Control Systems with Distributed Parameters (I)*

[Miron, Cristian](#) Pol. Univ. of Bucharest  
[Popescu, Dumitru](#) Pol. Univ. of Bucharest  
[Petrescu, Catalin](#) Pol. Univ. of Bucharest

17:50-18:10, [Paper FrB2.6](#)

*Fuzzy Modeling and Control for a Road Section (I)*

[Dimon, Catalin](#) Pol. Univ. of Bucharest  
[Popescu, Dumitru](#) Pol. Univ. of Bucharest  
[Stefanoiu, Dan](#) Pol. Univ. of Bucharest

FrB3 Regular Session, Carpati

[Applied Informatics](#)

Chair: [Postolache, Mihai](#) Gheorghe Asachi Tech. Univ. of Iasi  
Co-Chair: [Ciobanu, Adrian](#) Inst. of Computer Science, Romanian Acad. Iasi Branch

16:10-16:30, [Paper FrB3.1](#)

*A Review of HDL-Based System for Real-Time Image Processing Used in Tumors Screening*

[Chiuchisan, Iuliana](#) Stefan Cel Mare Univ. of Suceava  
[Geman, Oana](#) Stefan Cel Mare Univ. of Suceava

16:30-16:50, [Paper FrB3.2](#)

*A Study on Automatic Recognition of Positive and Negative Emotions in Speech*

[Pavaloi, Ioan](#) Inst. of Computer Science, Romanian Acad. Iasi Branch  
[Ciobanu, Adrian](#) Inst. of Computer Science, Romanian Acad. Iasi Branch  
[Luca, Mihaela](#) Inst. of Computer Science, Romanian Acad. Iasi Branch  
[Musca, Elena](#) Inst. of Computer Science, Romanian Acad. Iasi Branch  
[Barbu, Tudor](#) Inst. of Computer Science, Romanian Acad. Iasi Branch  
[Ignat, Anca](#) Alexandru Ioan Cuza Univ. of Iasi

16:50-17:10, [Paper FrB3.3](#)

*Security Solution for Healthcare Hybrid Cloud Platform*

[Marcu, Roxana Elena](#) Pol. Univ. of Bucharest

17:10-17:30, [Paper FrB3.4](#)

*Uniformity and Correlation Test Parameters for Random Numbers Generators*

[Petrla, Lulian](#) Gheorghe Asachi Tech. Univ. of Iasi  
[Manta, Vasile](#) Gheorghe Asachi Tech. Univ. of Iasi  
[Ungureanu, Florina](#) Gheorghe Asachi Tech. Univ. of Iasi

17:30-17:50, [Paper FrB3.5](#)

*Artificial Intelligence Application Built for ATS Detection with a New Portable Hollow Fiber IRAS Spectrometer*

[Praisler, Mirela](#) Dunarea De Jos Univ. of Galati  
[Ciochina, Stefanut](#) Dunarea De Jos Univ. of Galati  
[Stoica, Atanasia](#) Dunarea De Jos Univ. of Galati

17:50-18:10, [Paper FrB3.6](#)

*CFD Simulation of the Airflow Pattern within a Three-Bed Hospital Room with or without a Portable Air Conditioner in Use*

[Vladut, Gabriel](#) S.C. IPA CIFATT Craiova  
[Sbirna, Liana Simona](#) Univ. of Craiova  
[Sbirna, Sebastian](#) St. Stephen Ec. School of Craiova  
[Codresci, Cristian](#) Sodinal Romania  
[Martin, Liviu](#) Turceni City Hospital

FrB4 Regular Session, Laca

**Control Applications**

Chair: [Valean, Honoriu](#) Tech. Univ. of Cluj-Napoca  
Co-Chair: [Nitulescu, Mircea](#) Univ. of Craiova

16:10-16:30, [Paper FrB4.1](#)

*Dynamics Properties and Control for Oilwell Drillstrings*

[Bobasu, Eugen](#) Univ. of Craiova  
[Ivanov, Sergiu](#) Univ. of Craiova  
[Popescu, Dan](#) Univ. of Craiova  
[Rasvan, Vladimir](#) Univ. of Craiova

16:30-16:50, [Paper FrB4.2](#)

*The Determination of the Maximum Energetic Zones for a Wind System, Operating at Variable Wind Speeds*

[Erdodi, Gheza-Mihai](#) Pol. Univ. of Timisoara  
[Petrescu, Doru-Ionut](#) Pol. Univ. of Timisoara  
[Sorandaru, Ciprian](#) Pol. Univ. of Timisoara  
[Musuroi, Sorin](#) Pol. Univ. of Timisoara

16:50-17:10, [Paper FrB4.3](#)

*Modelling of Bio-Products Conversion Processes for Pollutant Compounds Formation Dynamics Assessment*

[Roman, Monica](#) Univ. of Craiova  
[Selisteanu, Dan](#) Univ. of Craiova

17:10-17:30, [Paper FrB4.4](#)

*Parameter Estimation Techniques for a Rehabilitation Hand Exoskeleton*

[Ivanescu, Mircea](#) Univ. of Craiova  
[Popescu, Dorin](#) Univ. of Craiova  
[Nitulescu, Mircea](#) Univ. of Craiova  
[Popescu, Nirvana](#) Pol. Univ. of Bucharest

17:30-17:50, [Paper FrB4.5](#)

*Temperature Control of the Asphaltic Emulsion in an Industrial Tank*

[Muresan, Vlad](#) Tech. Univ. of Cluj-Napoca  
[Abrudean, Mihail](#) Tech. Univ. of Cluj-Napoca  
[Valean, Honoriu](#) Tech. Univ. of Cluj-Napoca  
[Colosi, Tiberiu](#) Tech. Univ. of Cluj-Napoca

17:50-18:10, [Paper FrB4.6](#)

*Variable DC Power Sources for 13C Isotope Separation Column Boiler Supply*

[Dulf, Eva Henrietta](#) Tech. Univ. of Cluj Napoca  
[Both, Roxana](#) Tech. Univ. of Cluj-Napoca  
[Munteanu, Radu A.](#) Tech. Univ. of Cluj-Napoca  
[Festila, Clement](#) Tech. Univ. of Cluj Napoca  
[Secara, Mihai](#) Tech. Univ. of Cluj-Napoca

FrB5 Regular Session, Bucegi 1

**Nonlinear Systems**

Chair: [Sima, Vasile](#) National Inst. for Res. and Development in Informatics, Bucharest  
Co-Chair: [Halas, Miroslav](#) Slovak Univ. of Tech.

16:10-16:30, [Paper FrB5.1](#)

*Modelling and Performance Analysis of an Urban Wastewater Treatment Plant*

[Luca, Laurentiu](#) Dunarea De Jos Univ. of Galati  
[Barbu, Marian](#) Dunarea De Jos Univ. of Galati  
[Caraman, Sergiu](#) Dunarea De Jos Univ. of Galati

16:30-16:50, [Paper FrB5.2](#)

*Eigenvalues for a Nonlinear Time-Delay System*

[Halas, Miroslav](#) Slovak Univ. of Tech. in Bratislava

16:50-17:10, [Paper FrB5.3](#)

*Control-Oriented Modeling and Flight Dynamics Analysis of a Flexible Generic Hypersonic Vehicle*

[Zhu, Jiao](#) Beihang Univ  
[Chen, Wanchun](#) Beihang Univ  
[Ma, Hongzhong](#) Beijing Electro-Mechanical Engineering Inst  
[Yang, Zhihong](#) Beijing Aerospace Tech. Inst

17:10-17:30, [Paper FrB5.4](#)

*Imperialist Competitive Algorithm with Variable Parameters for the Optimization of a Fuzzy Controller*

[Ciurea, Stelian](#) Lucian Blaga Univ. of Sibiu  
[Trifa, Viorel](#) Tech. Univ. of Cluj-Napoca

17:30-17:50, [Paper FrB5.5](#)

*[Nonlinear Fuzzy Control of Human Heart Rate During Aerobic Endurance Training with Respect to Significant Model Variations](#)*

[Patrascu, Adrian](#)

Babes-Bolyai Univ. of Cluj-Napoca

[Patrascu, Monica](#)

Pol. Univ. of Bucharest

[Hantiu, Jacob](#)

Babes-Bolyai Univ. of Cluj-Napoca

17:50-18:10, [Paper FrB5.6](#)

*[Sampled-Data Robust Feedback Linearization Using Neural Network-Aided Unscented Kalman Filter](#)*

[Zaheer, Asim](#)

National Univ. of Sciences and Tech. of Islamabad

[Hasan, Momena](#)

National Univ. of Sciences and Tech. of Islamabad

[Ali, Usman](#)

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[Salman, Muhammad](#)

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**COMPLETE LIST OF AUTHORS:** Lucian ANDRIES, Vasile Gheorghita GAITAN

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